# BT138F series

### GENERAL DESCRIPTION

Glass passivated triacs in a full pack plastic envelope, intended for use in applications requiring high bidirectional transient and blocking voltage capability and high thermal cycling performance. Typical applications include motor control, industrial and domestic lighting, heating and static switching.

# QUICK REFERENCE DATA

SYMBOL	PARAMETER	MAX.	MAX.	MAX.	UNIT
	BT138F- BT138F- BT138F-	500 500F 500G	600 600F 600G	800 800F 800G	
V <sub>DRM</sub>	Repetitive peak off-state	500	600	800	V
I <sub>T(RMS)</sub> I <sub>TSM</sub>	voltages RMS on-state current Non-repetitive peak on-state current	12 90	12 90	12 90	A A

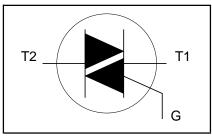
#### PINNING - SOT186

PIN	DESCRIPTION					
1	main terminal 1					
2	main terminal 2					
3	gate					
case	isolated					

# case

**PIN CONFIGURATION** 

#### SYMBOL



### LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.		MAX.		UNIT
V <sub>drm</sub>	Repetitive peak off-state voltages		-	<b>-500</b> 500 <sup>1</sup>	<b>-600</b> 600 <sup>1</sup>	<b>-800</b> 800	v
I <sub>T(RMS)</sub> I <sub>TSM</sub>	RMS on-state current Non-repetitive peak on-state current	full sine wave; $T_{hs} \le 56$ °C full sine wave; $T_j = 125$ °C prior to surge; with reapplied $V_{DRM(max)}$	-		12		A
		t = 20 ms	-		90		A
l <sup>2</sup> t	I <sup>2</sup> t for fusing	t = 16.7 ms t = 10 ms	-		100 40		A A <sup>2</sup> s
dl <sub>T</sub> /dt	Repetitive rate of rise of on-state current after	$I_{TM} = 20 \text{ A}; I_G = 0.2 \text{ A};$ $dI_G/dt = 0.2 \text{ A}/\mu \text{s}$	-		40		
	triggering	T2+G+	-		50		A/μs
		T2+G-	-		50		A/µs
		T2- G- T2- G+	-		50 10		A/μs A/μs
lau	Peak gate current	12- 6+					Â
I <sub>GM</sub> V <sub>GM</sub>	Peak gate voltage		-		2 5 5		V
P <sub>GM</sub>	Peak gate power		-		5		Ŵ
$\begin{array}{c} P_{G(AV)}^{G(AV)} \\ T_{stg} \\ T_{j} \end{array}$	Average gate power Storage temperature Operating junction temperature	over any 20 ms period	- -40 -		0.5 150 125		°℃ Ú

<sup>1</sup> Although not recommended, off-state voltages up to 800V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 15  $A/\mu s$ .

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## **ISOLATION LIMITING VALUE & CHARACTERISTIC**

 $T_{hs} = 25$  °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V <sub>isol</sub>	Repetitive peak voltage from all three terminals to external heatsink	$R.H. \leq 65\%$ ; clean and dustfree	-		1500	V
C <sub>isol</sub>	Capacitance from T2 to external heatsink	f = 1 MHz	-	12	-	рF

## THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
R <sub>th j-hs</sub> R <sub>th j-a</sub>	Thermal resistance junction to heatsink Thermal resistance junction to ambient	full or half cycle with heatsink compound without heatsink compound in free air		- - 55	4.0 5.5 -	K/W K/W K/W

## STATIC CHARACTERISTICS

 $T_j = 25$  °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.		MAX.		UNIT
I <sub>GT</sub>	Gate trigger current	<b>BT138F-</b> V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A				F	G	
GI		T2+G+ T2+G-	-	5 8	35 35	25 25	50 50	mA mA
		T2- G- T2- G+	-	10 22	35 70	25 70	50 100	mA mA
IL.	Latching current	$V_{\rm D} = 12 \text{ V}; \text{ I}_{\rm GT} = \begin{array}{c} 12^{-1} \text{ G}^{+1} \\ 0.1 \text{ A} \\ 12^{+1} \text{ G}^{+1} \end{array}$		7	40	40	60	mA
		T2+ G+ T2+ G- T2- G-	-	20 8	60 40	60 40	90 60	mA mA
   I <sub>H</sub>	Holding current	$V_{\rm D} = 12 \text{ V}; \text{ I}_{\rm GT} = 0.1 \text{ A}$	-	10 6	40 60 30	60 60 30	90 60	mA mA
V <sub>T</sub> V <sub>GT</sub>	On-state voltage Gate trigger voltage	$I_{T} = 15 \text{ A}$ $V_{D} = 12 \text{ V}; I_{T} = 0.1 \text{ A}$	-	1.4 0.7		1.65 1.5	I	
I <sub>D</sub>	Off-state leakage current		0.25 -	0.4 0.1		- 0.5		mA

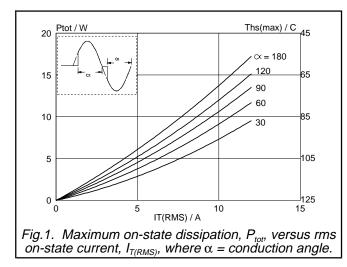
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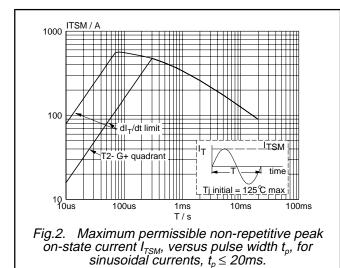
### **DYNAMIC CHARACTERISTICS**

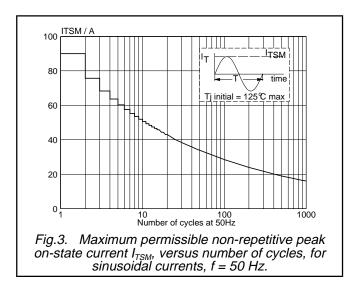
 $T_j = 25$  °C unless otherwise stated

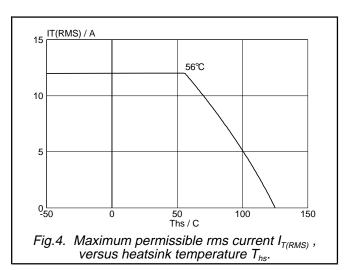
SYMBOL	PARAMETER	CONDITIONS		MIN.		TYP.	MAX.	UNIT
dV <sub>D</sub> /dt	Critical rate of change of off-state voltage	BT138F- $V_{DM} = 67\% V_{DRM(max)};$ $T_i = 125 °C; exponential$	 100	<b>F</b> 50	<b>G</b> 200	250	-	V/µs
dV <sub>com</sub> /dt	Critical rate of change of commutating voltage	waveform; gate open circuit $V_{DM} = 400 \text{ V}; \text{ T}_{j} = 95 ^{\circ}\text{C};$ $I_{T(RMS)} = 12 \text{ A};$ $dI_{com}/dt = 5.4 \text{ A/ms}; gate$	-	-	10	20	-	V/µs
t <sub>gt</sub>	Gate controlled turn-on time	open circuit $I_{TM} = 16 \text{ A}; V_D = V_{DRM(max)};$ $I_G = 0.1 \text{ A}; dI_G/dt = 5 \text{ A}/\mu \text{s}$	-	-	-	2	-	μs

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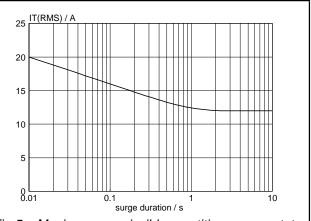
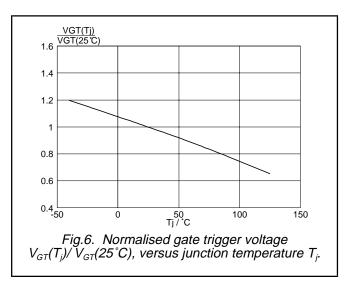
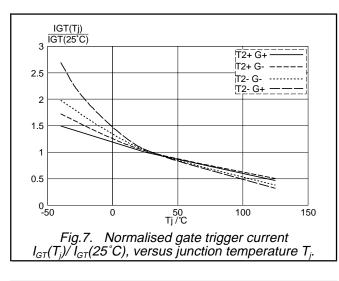
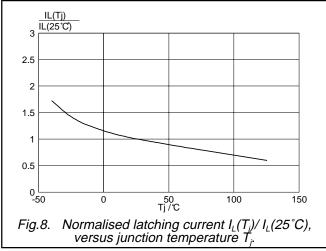


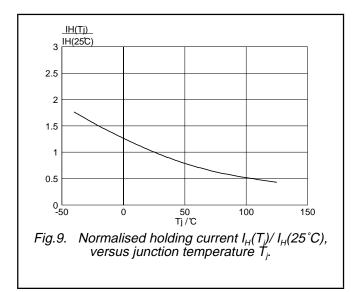
Fig.5. Maximum permissible repetitive rms on-state current  $I_{T(RMS)}$ , versus surge duration, for sinusoidal currents, f = 50 Hz;  $T_{hs} \le 56$ °C.

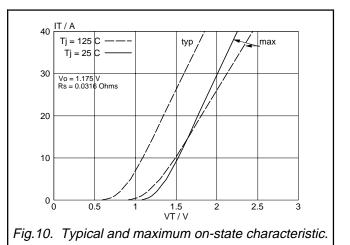


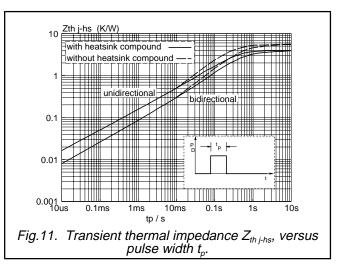
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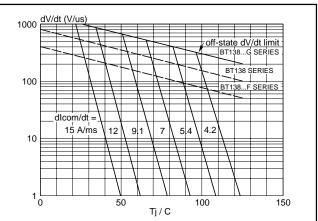


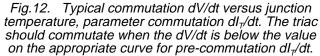






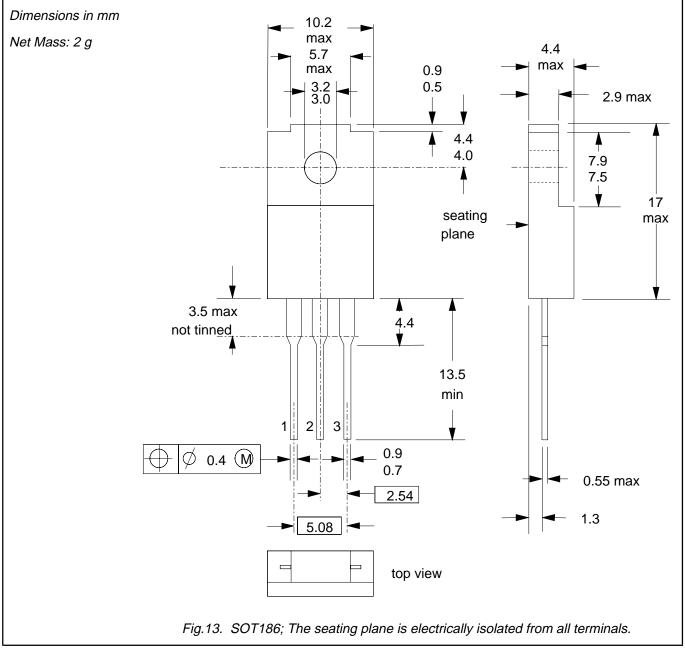






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#### **MECHANICAL DATA**



#### Notes

Accessories supplied on request: refer to mounting instructions for F-pack envelopes.
Epoxy meets UL94 V0 at 1/8".

#### DEFINITIONS

Data sheet status						
Objective specification This data sheet contains target or goal specifications for product development.						
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.					
Product specification	This data sheet contains final product specifications.					
Limiting values						
or more of the limiting val operation of the device at	Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.					
Application information						
Where application information is given, it is advisory and does not form part of the specification.						
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